





2025 IEEE International Memory Workshop May 18th – 21st, 2025, Monterey, CA, USA

CALL FOR PAPERS (Deadline February 3rd, 2025)

The 17th IEEE International Memory Workshop (IEEE IMW) is currently planned as an on-site event from May 18th – May 21st, 2025 in Monterey, CA, USA. The conference brings the memory community together in a workshop environment to discuss the memory process and design technologies, applications, market needs and strategies. It is sponsored by the IEEE Electron Devices Society and meets annually in May.

The IEEE IMW is a premier international forum for both new and seasoned technologists having diverse technical backgrounds to share and learn about the latest developments in memory technology with the global community. The scope of workshop content ranges from new memory concepts in early research to the technology drivers currently in volume production as well as emerging technologies in development.

The workshop will start on Sunday, May 18th with a Tutorial Session, followed by Technical Sessions from Monday May 19th through Wednesday May 21st. The evening panel discussions will address hot topics in the memory field and active participation and discussions will be encouraged.

Papers are solicited in all aspects of semiconductor memory technology (Flash, DRAM, SRAM, PCRAM, RRAM, MRAM, FRAM, Embedded Memories, System, and Emerging Memories), including but are not limited to:

Device Physics & Modeling Cell Design & Novel Materials Memory Process & Integration Circuit Design, Algorithms & Error Management Quality & Reliability System Architecture New Concepts & Disruptive Technologies Emerging Applications & Markets SSD, Mobile & Automotive In-Memory & Neuromorphic Computing Memory-enabled Artificial Intelligence

PAPER SUBMISSION

Single submission of final, up to four-page paper

Best Paper and Best Student Paper will be awarded at the end of the Workshop

- Notification of acceptance: March 17th, 2025
 - Invited talks given by renowned experts

Please submit an electronic paper in PDF format at: <u>https://imw2025.exordo.com/</u>. The suggested format can be found at: <u>https://www.ieee.org/conferences/publishing/templates.html</u>. The paper should be no more than four pages including supporting figures and tables. **The deadline for submitting paper is February 3rd, 2025**. The proceedings with all the accepted papers will be provided to the participants as a secured PDF download. It will also be submitted to the IEEE for submission to the Xplore database for easy referencing. In keeping with the workshop nature of the meeting, no photograph or recording will be allowed. Submissions from universities are encouraged. There is a subsidized registration fee for students and IEEE Members. The maximum attendance at the workshop is limited. Therefore, advance registration is highly recommended.

CONTACT INFORMATION Technical Chair General Chair Finance Chair Publicity Chair Haitao Liu Sangbum Kim Prashant Majhi Antonio Arreghini Micron Seoul National Intel **IMEC**, Belgium Technology University Hliu@micron.com sangbum.kim@snu.ac.kr prashant.majhi@intel.com Antonio.Arreghini@imec.be

If you have questions, please email at: <u>imw.sangbum.kim@gmail.com</u> or visit the IEEE IMW website at: http://www.ewh.ieee.org/soc/eds/imw/